

DIGITAL-TO-PHASE-CONVERTER

ABSTRACT OF THE DISCLOSURE

A digital-to-phase or digital-to-time-shift converter (100) includes a delay
5 line (106), a multiplexor (108) and synchronization circuit (110). In the converter
(100) the clock edges of a reference signal are shifted in response to the value of
a multi-bit digital word, IN (104). The synchronization circuit (110) gates the
output of the multiplexor (108) such that a pulse appears at the synchronization
circuit's (110) output port (114) only when the circuit is gated by a signal at input
10 TRIG (112). The synchronization circuit (110) creates a time aperture for the
multiplexor output.